



Patent  
Orrick Docket No.: 700693.4022

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Michael R. BUTTS et al.

Serial No.: 10/614,537

Filed: July 3, 2003

For: **SYSTEM AND METHOD FOR  
PERFORMING DESIGN VERIFICATION**

)  
) Group Art Unit: Not Yet Assigned

)  
) Examiner: Not Yet Assigned

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

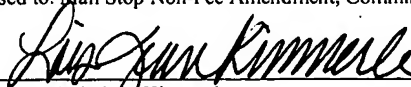
In accordance with 37 CFR §§ 1.97 and 1.98, the items identified in this Information Disclosure Statement ("IDS") are brought to the attention of the Office. The items are listed on the attached form PTO-1449 and copies are enclosed for the convenience of the Examiner.

The items identified in this IDS may or may not be "material" pursuant to 37 CFR § 1.56. The submission thereof by Applicant is not to be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as "prior art" under 35 USC § 102 with respect to this invention unless specifically designated by Applicant as such.

**CERTIFICATE OF MAILING**  
(37 C.F.R. §1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 25, 2003  
Date of Deposit

  
Lois Jean Kimmerle

DOCSOC1:144933.1  
700693-4022 D2S

**INFORMATION DISCLOSURE STATEMENT FILING PROVISION:**

☒ This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d); or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.

☒ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.

☐ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.

☐ This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.

☐ The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.

☐ This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.

**STATEMENT UNDER 37 CFR § 1.97(e):**

☐ Each item contained in this IDS was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.

☐ No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.

**PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:**


- ☐ A check in the amount of \_\_\_\_\_ is enclosed for the above fee(s).  
☐ Please charge \_\_\_\_\_ to Deposit Account No. **15-0665** for the above fee(s).

The Commissioner is authorized to charge any fees required by the filing of these papers, and to credit any overpayment to Orrick, Herrington & Sutcliffe's Deposit Account No. **15-0665**.

Respectfully submitted,  
Orrick, Herrington & Sutcliffe LLP

Dated: November 25, 2003

By:

  
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LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT

## APPLICANT:

Michael R. Butts et al.

## FILING DATE:

July 3, 2003

## GROUP:

Not Yet Assigned

(Us s v ral she ts if n c ssary)

NOV 28 2003

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
AI						
AJ						
AK						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AL	ProDesign GmbH, "CHIPit Gold Edition," Copyright 2003, 2 pages, available at <a href="http://www.chipit.de/ce/CHIPitGoldEdition_UMRbus.htm">http://www.chipit.de/ce/CHIPitGoldEdition_UMRbus.htm</a>
AM	ProDesign GmbH, "Speeding Verification of Custom Designs," Embedded Systems Europe, June 2003, 2 pages, available at <a href="http://www.chipit.de/ce/pdf/whitepaper/Embedded%20Europe.pdf">http://www.chipit.de/ce/pdf/whitepaper/Embedded%20Europe.pdf</a>
AN	ProDesign GmbH, "Crack the Bottleneck Problem," March 2002, 4 pages, available at <a href="http://www.chipit.de/ce/pdf/whitepaper/crack.pdf">http://www.chipit.de/ce/pdf/whitepaper/crack.pdf</a>
AO	Aptix Corporation, "Module Verification Platform," Product Brief, Fourth Quarter 2001, 2 pages
AP	SynaptiCAD, Inc., "PinPort links Hardware to HDL Simulations," Copyright 2001, 3 pages, available at <a href="http://www.syncad.com/pinport_main.htm?SynaptiCADSessionID=515fc200d23dcc19578a3f87f2fef366">http://www.syncad.com/pinport_main.htm?SynaptiCADSessionID=515fc200d23dcc19578a3f87f2fef366</a>

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## EXAMINER:

## DATE CONSIDERED:

EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant